

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Protigal et al.

Serial No.: Not Yet Assigned

Filed: October 1, 2003

For: INTEGRATED CIRCUIT MODULE
HAVING ON-CHIP SURGE CAPACITORS

Confirmation No.: Unknown

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 2269-2898.4US
(88-0070.09/US)

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The present application is a continuation of application Serial No. 09/776,387, filed February 2, 2001, pending, which is a continuation of U.S. Patent Application Serial Number 08/965,741, filed November 7, 1997, now U.S. Patent 6,184,568, issued February 6, 2001, which is a continuation of U.S. Patent Application Serial Number 08/671,248, filed June 27, 1996, now U.S. Patent 5,687,109, which is a continuation of U.S. Patent Application Serial Number 08/178,716, filed January 10, 1994, now abandoned, which is a continuation of U.S. Patent Application Serial Number 08/034,001, filed March 19, 1993, which is now U.S. Patent 5,307,309, which is a continuation of U.S. Patent Application Serial Number 07/774,121, filed October 8, 1991, abandoned, which is a continuation of U.S. Patent Application Serial Number

Attorney Docket No. 2269-2898.4US

07/291,294, filed December 27, 1988, now abandoned, which is a continuation-in-part of U.S. Patent Application Serial Number 07/200,673, filed May 31, 1988, now abandoned.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the eight prior applications, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 or PTO/SB/08 form listing all of the information of record in the prior applications is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'K. K. Johanson', with a long horizontal line extending to the right.

Kevin K. Johanson
Registration No. 38,506
Attorney for Applicants
TRASKBRITT
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

Date: October 1, 2003

KKJ/dlm:rmh

Enclosures: Form PTO-1449 or PTO/SB/08

Document in ProLaw

Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>	Docket Number (Optional) 2898.4US (88-070.09/US)	Application Number Not Yet Assigned
	Applicant Protigal et al.	
	Filing Date October 1, 2003	Group Art Unit Unknown

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,460,010	08/1969	R.J. Domenico et al.			
	3,538,397	11/1970	S.P. Davis			
	3,566,203	02/1971	Maguire et al.			
	3,699,722	10/1972	Davidson et al.			
	3,769,105	10/1973	Chen et al.			
	3,772,097	11/1973	Davis			
	3,893,146	07/1975	Heeren			
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	4,164,751	08/1979	Tasch, Jr.			
	4,266,282	05/1981	Henle et al.			
	4,271,461	06/1981	Hoffmann et al.			
	4,317,686	03/1982	Anand et al.			
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FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	56-15065	02/1981	Japan				
	56-148859	11/1981	Japan				
	56-153778	11/1981	Japan				
	57-001902	01/1982	Japan				
	2 103 012 A	07/1982	GB				

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		McKenny, Verron D., "Good bits swapped for bad in 64-kilobit E-PROM," pps. 115-120 plus Cover and Table of Contents, <u>Electronics</u> , March 13, 1990.
		Natori et al., "FAM: 16.5: A 34ns 256K DRAM," pps. 232-233 plus Cover and Table of Contents, <u>IEEE International Solid-State Circuits Conference</u> , February 25, 1983.
		<u>Research Disclosure</u> , (3 pages), June 1985.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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	Applicant Protigal et al.	
	Filing Date October 1, 2008	Group Art Unit Unknown

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	4,389,715	06/1983	Eaton, Jr. et al.			
	4,419,812	12/1983	Topich			
	4,423,431	12/1983	Sasaki			
	4,427,989	01/1984	Anantha et al.			
	4,458,336	07/1984	Takemae			
	4,477,736	10/1984	Onishi			
	4,493,056	01/1985	Mao			
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	4,567,542	01/1986	Shimada et al.			
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	4,587,639	05/1986	Aoyama et al.			
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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	58-64048	04/1983	Japan				
	58-77251	05/1983	Japan				
	61-73367	04/1986	Japan				
	61-082458	04/1986	Japan				

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		"Decoupling Capacitor Structure to Reduce FET Output Driver Switching Noise," pps. 167-168, <u>IBM Technical Disclosure Bulletin</u> , Vol 30., No. 7, December 1987.
		"MOS Memory," (4 pages), <u>Toshiba Mos Memory Products Data Book</u> , April 1987.
		"Double-Traversing Pseudo-Folded-Bitline Design for Cross-Point Memory Cells," pps. 246-248, <u>IBM Technical Disclosure Bulletin</u> , Vol. 30, No. 11, April 1988.

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Form PTO-1449

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)
2898.4US (88-070.09/US)

Application Number
Not Yet Assigned

Applicant **Protigal et al.**Filing Date **October 1, 2003**Group Art Unit **Unknown**

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	4,605,980	08/1986	Hartranft et al.			
	4,616,655	10/1986	Weinberg et al.			
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	4,720,467	01/1988	Muggli et al.			
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		"New Memory Products 1987/1988," (4 pages), <u>NEC Electronics Inc. Supplement to 1986 Memory Products Data Book</u> , December 1987.
		Gillingham et al., "High-Speed, High-Reliability Circuit Design for Megabit DRAM," pps. 1171-1175 plus Table of Contents, <u>IEEE Journal of Solid-State Circuits</u> , Vol 26, No. 8, August 1991.
		"IBM's Response to Micron," (22 pages), December 8, 1995.

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			Filing Date October 1, 2003		Group Art Unit Unknown	

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	4,737,830	04/1988	Patel et al.			
	4,777,518	10/1988	Mihara et al.			
	4,780,846	10/1988	Tanabe et al.			
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	4,780,851	10/1988	Kurakami			
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	4,829,480	05/1989	Seo			
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	4,903,113	02/1990	Frankeny et al.			
	4,930,112	05/1990	Tanaka et al.			
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	5,012,132	04/1991	Wang			
	5,014,097	05/1991	Kazerounian et al.			

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		"On-Chip Decoupling Capacitor for Logic VLSI Chips," page 1280, IBM Technical Disclosure Bulletin, Vol. 30 No. 3, Aug 1987.

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	5,032,892	07/1991	Chern et al.			
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	5,366,931	11/1994	Kim			
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	5,687,109	11/1997	Protigal et al.			
	RE 35,825	06/1998	Zager			
	6,184,568	02/2001	Protigal et al.			

FOREIGN PATENT DOCUMENTS							
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						YES	NO
	61-269317	11/1986	Japan				
	01-268032	10/1989	Japan				

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